

Accelerating Understanding

HPC RoadShow

Soluzioni HPC Dell

Le esperienze di due Atenei Europei a confronto: Università di Napoli Federico II e University of Cambridge



Napoli, 5 maggio 2011

Centro Congressi di Monte Sant'Angelo – Università di Napoli Federico II



Event in numbers



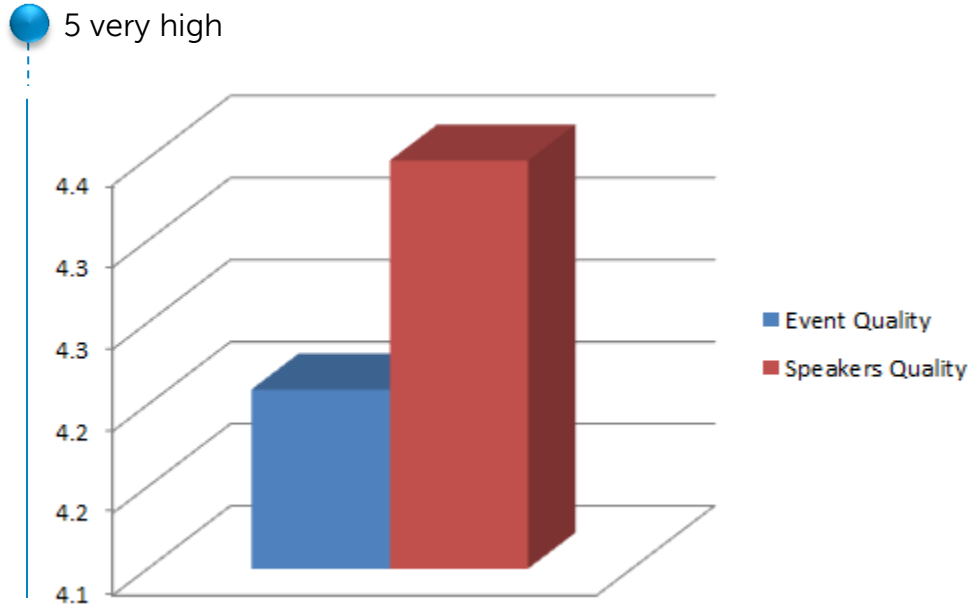
- 800+ invitations
- 192* onsite registered people
- 156 online registrations
- 138 present attendees (peak)
- 6 Academic Institutions
- 2 Local Public Administrations
- 6 Research Organizations

- 2 University of Naples speeches
- 1 University of Cambridge speech
- 2 Dell speeches
- 3 Dell Partners speeches
- 1 Guided Tour at the SCoPE datacenter



** staff included: Dell, Intel, Cambridge, Mellanox, NVidia*

Quality of this Event



Some quote

“ Has been great to see an experience exchange between two important Universities like Naples Federico II and Cambridge ”

“ Nice event, happy to see a possible collaboration with Cambridge, learning from their experience ”

“ We love Dell events when built around innovation and full of high quality contents ”

From our analysis of collected feedback forms, we may state that the perception of the general quality of this event has been more than high (4.2), as well as the quality of the speakers (4.3)

Invitations



- Invites have been sent from University of Naples Federico II and Dell
- Invitation has been extended to the whole Dell Italy HPCC Community (more than 700 people interested in High-Performance Computing)



Dell invitation



University of Naples invitation

Institutional Presence



University of Naples Federico II

- Prof. Leonardo Merola - Presidente del Comitato Scientifico di SCoPE
- Prof. Guido Russo - Comitato Scientifico di SCoPE
- Prof. Roberto Pettorino – Preside Facoltà Scienze MFN

University of Cambridge

- Dr. Paul Calleja, Head of HPC Services

Dell

- Ing. Francesco Magri, Sales Director for Italian Public Administrations

Speakers^{/1}

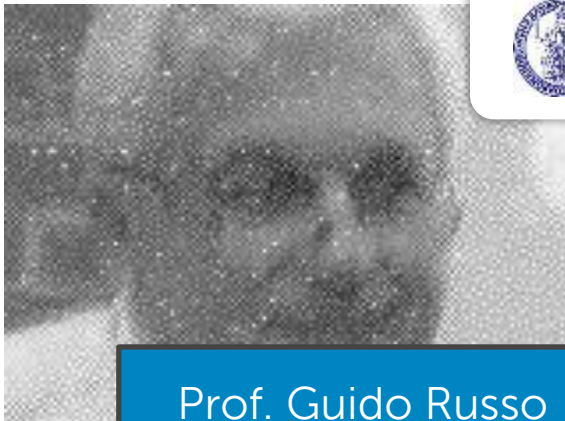
University of Naples Federico II, and Dell



Prof. Roberto
Pettorino



Prof. Leonardo Merola



Prof. Guido Russo



Francesco Magri (Dell)

Speakers^{/2}

Dell, Intel, University of Cambridge, Mellanox and NVidia



Paul Calleja
(Cambridge)



Nir Gal (Mellanox)



Marco Soldi (Intel)



Edmondo Orlotti
(NVIDIA)



Paolo Bianco (Dell)



Ivan Renesto (Dell)

Who joined the Event

Academic Institutions

- Università di Cambridge
- Università di Napoli Federico II
 - AOU
 - CSI
 - CIMCF
 - DIBET
 - Dip. di Ingegneria Aerospaziale
 - Dip. di Chimica
 - Dip. di Ingegneria Navale
- Seconda Università di Napoli
- Università degli Studi di Napoli Parthenope
- Università di Salerno
- Università di Palermo
 - Dipartimento di Fisica

Local Public Administrations

- Amministrazione Provinciale di Benevento
- ANM - Azienda Napoletana Mobilità

Research Organizations

- CIRA
- CNR
- INAF – Osservatorio Astronomico di Capodimonte
- INFN
- INGV
- Consorzio Technapoli

Companies

- Chorus Srl
- Euro.soft Srl
- IIT@CRIB
- Innova SpA
- Pico Solutions
- Softeco Sismat
- Software Design SpA
- SUN/CROM

Some Picture









Moore's Law is Driving and Democratizing HPC



1997

1 TFLOPS

2009

>500 TFLOPS

>200x

more performance

Performance of a 1 TFLOPS system is equivalent to 1,000,000,000,000 operations per second. This is a significant increase in performance over the 1997 system, which was only 1 TFLOPS. The increase in performance is due to Moore's Law, which states that the number of transistors on a chip doubles every two years.













